

DMAC

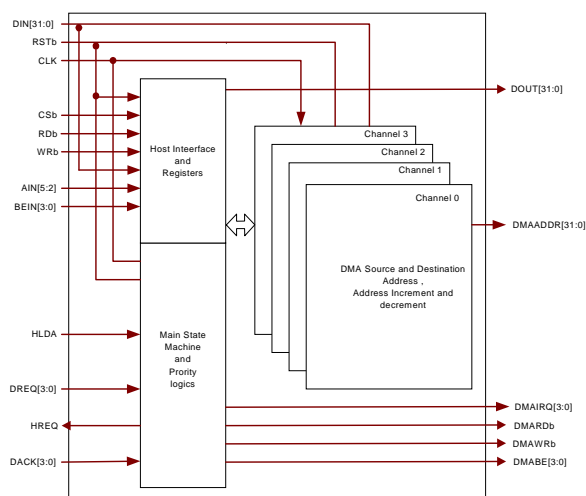
Direct Memory Access Controller (DMAC) Intellectual Property (IP).

Description

Four-channel direct memory access controller (DMAC). The DMAC can be used in place of the CPU to perform high-speed data transfers among external devices equipped with DACK (transfer request acknowledge signal), external memories modules. The Direct Memory Access Controller (DMA) Module provides a quick and efficient process for moving blocks of data with minimal processor overhead. The DMA module provides two channels that allow byte, word, or long word operand transfers. These transfers can be single address to off-chip devices or dual address to on-chip devices.

Features

- Independent source and destination address registers
- Four Gbytes of address space in the architecture
- Byte, word, or longword selectable data transfer unit
- Single address mode: Either the transfer source or transfer destination (peripheral device) is accessed by a DACK signal while the other is accessed by address. One transfer unit of data is transferred in each bus cycle.
- Source address reload function operates
- Supports burst and cycle steal transfers



Application Area : improve system performance by allowing external devices to directly transfer information from the system memory.

Available Documents : Data Book, Users Guide

Design File Formats : EDIF File Format , VHDL Code

Verification : Verilog Testbench

Simulation Tool Used : Model Technology ModelSim™ 5.4

For more information

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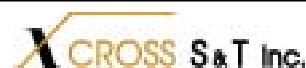
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