

X-CORE

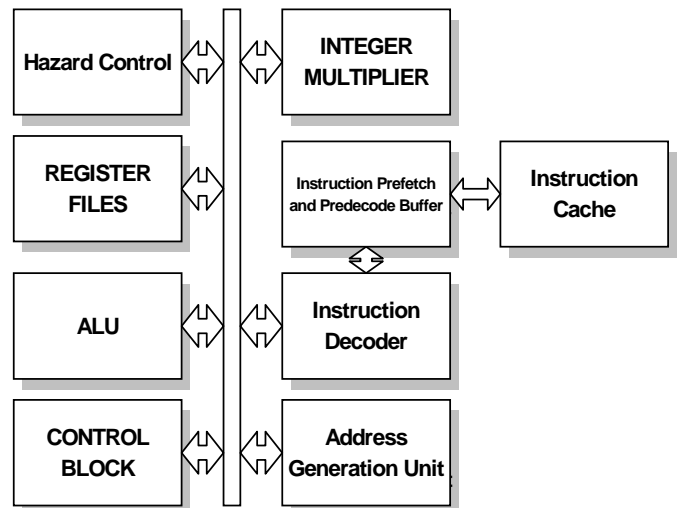
High Performance 32 bit Embedded CORE Intellectual Property (IP).

Description

X_CORE is suitable for the embedded processing application. It has powerful instruction for embedded application. And it has instruction pre-fetch buffer and pre-decode for fast operation. Optimized C compiler is supplied for X-CORE.

Features

- Soft Core.
- 16 Bit Fixed Instruction Set Architecture
- Ultra Low power consumption
- General Purpose Registers : 32 * 14
- Special Purpose Registers : 32 * 2
- 32 Bit ALSU (includes 32 bit barrel Shifter)
- Little Endian Modes
- Support 4 Co-Processors
- 4 Stage Pipeline
- Include instruction pre-fetch buffer and pre-decode for fast operation
- 8 K Bytes 2 way set associative instruction cache
- Performance(MIPS) is depend on the ASIC technology.



Application Area	: Embedded System control, Audio processing, video processing, STB, HDD, Mass storage application
Available Documents	: Data Book, Users Guide, program reference manual
Available Software	: Multi-platform optimized C/C++ Compiler, Assembler, Loader for LINUX, Windows, SUN etc.
Design File Formats	: EDIF File Format , Target FPGA./ASIC NETLIST, VHDL Code
Verification	: Verilog Testbench
Simulation Tool Used	: Model Technology ModelSim™ 5.4

For more information

CROSS S&T Inc. #715 Hyundai Office Bldg. 9-4, Sunae-Dong, Pundan-Ku, Sungnam, Kyunggi-Do, 463-783 Korea

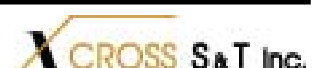
Tel : 82-31-713-9143

Web Site : <http://www.crosssemi.com>

Fax: 82-31-713-9144

Contact To : sales@crosssemi.com

Copyright (c) 2002 Cross S&T Inc. All rights reserved.

 CROSS S&T Inc.

* Contents described in this material are subject to change without notice for improvements and upgrades.